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The flow is centered around Denali PureSpec verification within the Cadence Incisive Verification Platform. PureSpec is a

http://www.cadence.com/alliances/ip_program/pages/denali.aspx

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Denali's PureSpec used by Agere for PCI Express interface verification.: An article from: Software Industry Report on Amazon.com. *FREE* shipping on qualifying offers.

<http://www.amazon.com/Denalis-PureSpec-Express-interface-verification/dp/B00082NA8Y>

Oct 31, 2005 PCI Express switch. Unisys selects Denali for PCI Express interface development. NEXTIO SELECTS DENALI PURESPEC FOR PCI VERIFICATION.

<http://www.thefreelibrary.com/PCI+Express+switch.-a0142433469>

> NextIO Selects Denali PureSpec for PCI Express Design Verification has adopted Denali Software's MMAV verification the semiconductor industry's

http://sist.afriseb.net/rubrique.php3?id_rubrique=167

PCI Express Interface Directory: Unisys Selects Denali for PCI Express Unisys engineers are using Denali's PureSpec verification IP to model,
<http://www.thaiio.com/interface-catalog/pci-express-interface/>

No more missed important software updates! UpdateStar 10 lets you stay up to date and secure with the software on your computer.

<http://client.updatestar.com/en/updatestar/download/>

Denali Launches Blueprint, New Software for System-on-Chip Design. Denali's PureSpec Verification IP product supports all complex interfaces,

<http://www.chipdesignmag.com/print.php?articleId=142?issueId=0>

Find out information about cellular multiprocessing. Unisys selects Denali for PCI Express interface development by EDP Weekly's IT Monitor.

<http://encyclopedia2.thefreedictionary.com/Cellular+multiprocessing>

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<http://ufdc.ufl.edu/UF00028315/00962>

Denali's PureSpec Verification IP product supports all complex interfaces, including PCI Express, Advanced Switching Interconnect (ASI), USB, and SATA II.

<http://discobolusdesigns.com/personal/DMR-Jan05.pdf>

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Denali's unique Databahn physical interface minimizes expensive, PureSpec PCI Express is the industry leading solution for verifying compliance

http://www.newplus.com.cn/products/denali/Datasheet_DRAM_5.pdf

Explanation of PCI bridge. PCI bridge Denali's PureSpec used by Agere for PCI Express interface verification by EDP Weekly's IT Monitor.

<http://encyclopedia2.thefreedictionary.com/PCI+bridge>

(EDA) software and intellectual property (IP), today announced Denali's PureSpec(TM) verification IP support for IBM's PowerPC(R) processor local bus

http://www.redorbit.com/news/technology/1754381/denali_announces_purespec_solution_featured_in_ibms_new_powerpc_plb6/

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